



- (577) microprocessor and s
- (390) (microprocessor and :
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- (7) (semiconductor and (so

Search

DBs: USPAT; EPO; JPO

Default operator: OR

☒ Plurals☒ Highlight all hit terms initially

semiconductor and (solder adj bump\$1 and bus\$2 and metal and window\$1)

BRS f...

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6548393 B1	20030415	28	Semiconductor chip assembly with hardened connection	438/618	257/693; 257/778;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6537851 B1	20030325	38	Method of connecting a bumped compliant conductive	438/107	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6509992 B1	20030121	11	Free space optical interconnect system tolerant	359/159	359/153
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6492252 B1	20021210	59	Method of connecting a bumped conductive trace to a	438/612	257/783
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6483116 B1	20021119	15	High performance ultraviolet imager for operation at room	250/372	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6476374 B1	20021105	16	Room temperature, low-light-level visible	250/214.1	257/444
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6464687 B1	20021015	35	Implantable drug delivery system	604/891.1	
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6442033 B1	20020827	10	Low-cost 3D flip-chip packaging technology for	361/743	174/52.1; 257/707;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6440835 B1	20020827	44	Method of connecting a conductive trace to a	438/611	438/107
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6412072 B2	20020625	36	Parasitically powered microprocessor capable of	713/200	
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6411494 B1	20020625	34	Distributed capacitor	361/306.3	361/306.1; 361/311;

Hits Start

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